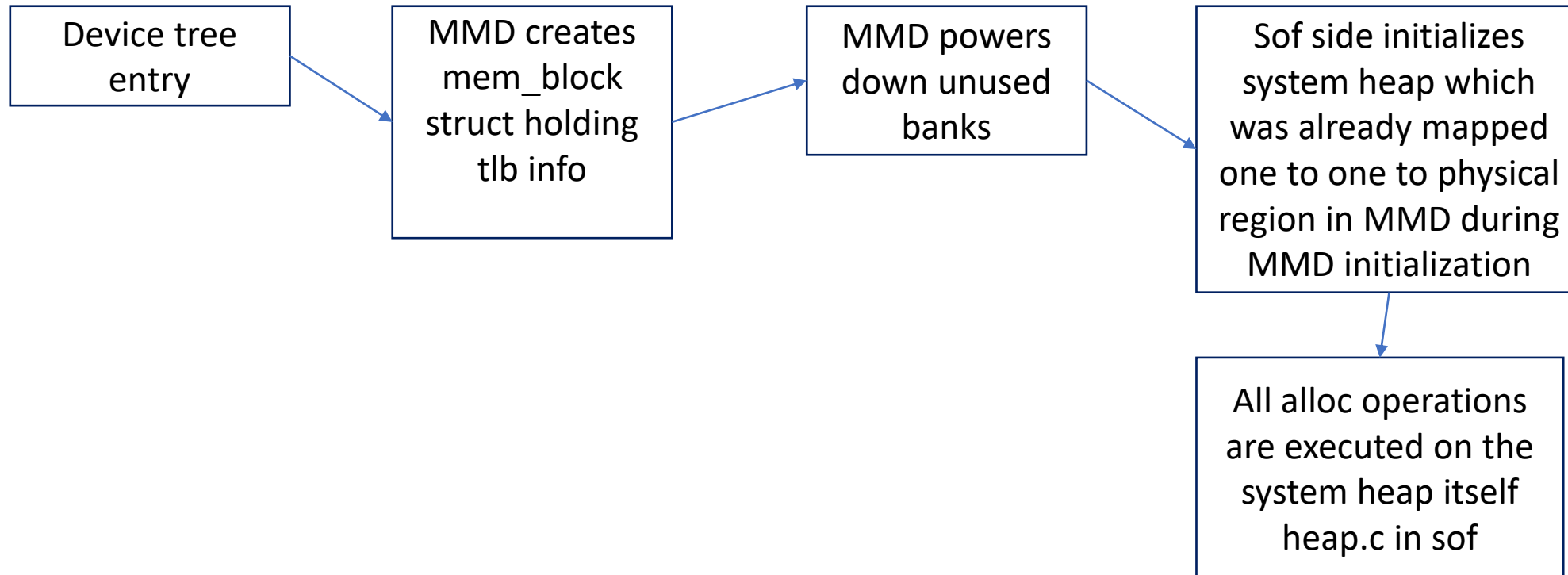
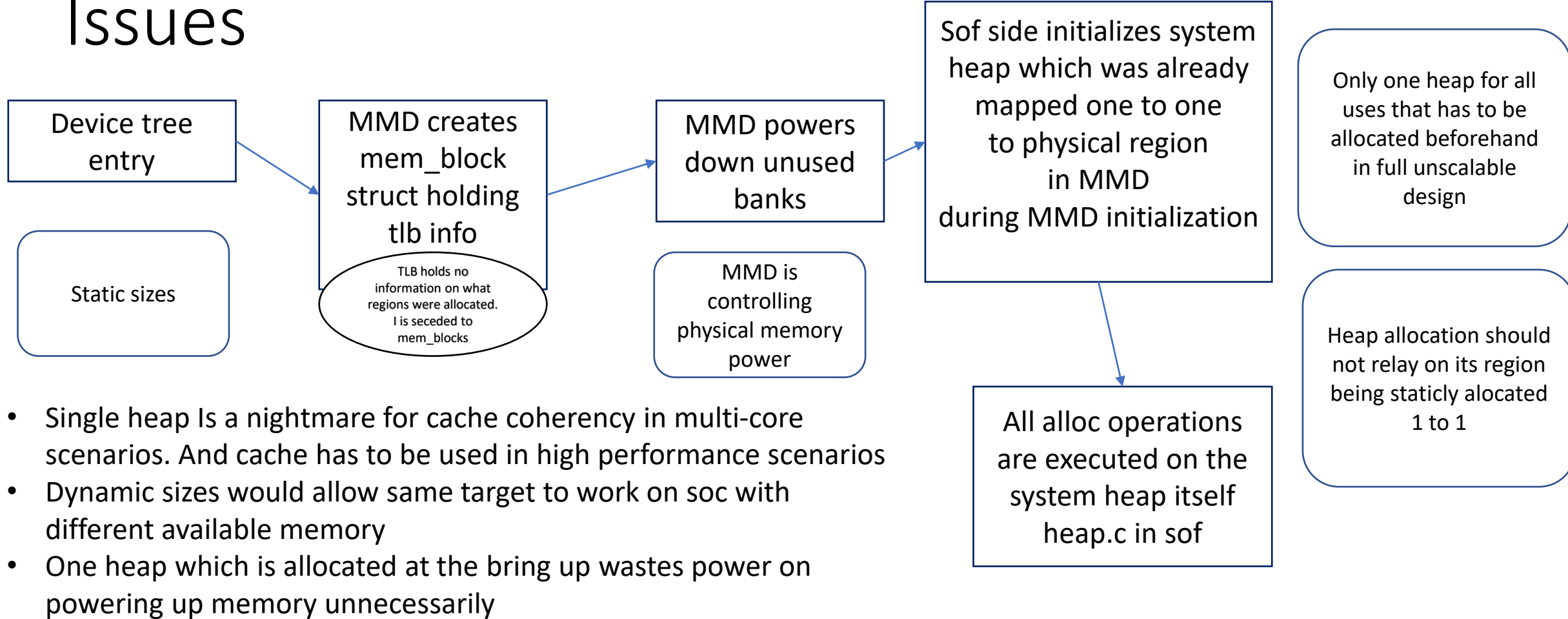


Current allocation flow



Issues



Proposition

DSP Core #0 Memory Block

System Heap (shared)

Devices

Application Heap (shared)

Shared buffers

Application Heap (local)

Pipelines @Core #0

LL Modules & Tasks @Core #0

DP Modules & Tasks @Core #0

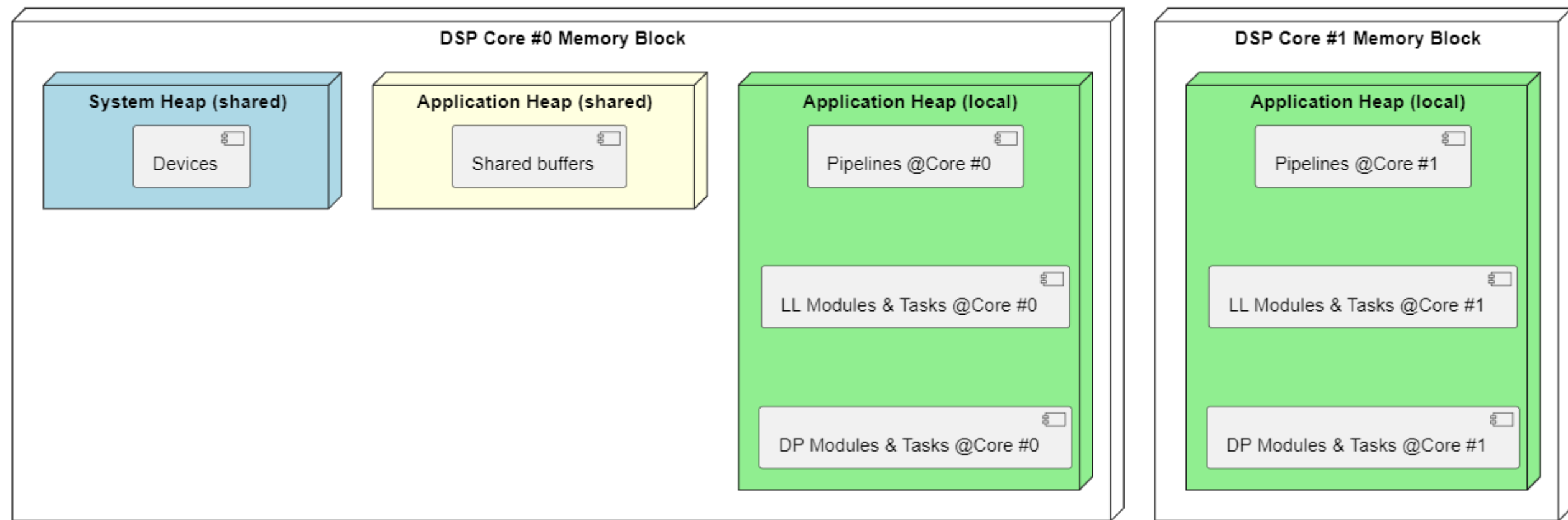
DSP Core #1 Memory Block

Application Heap (local)

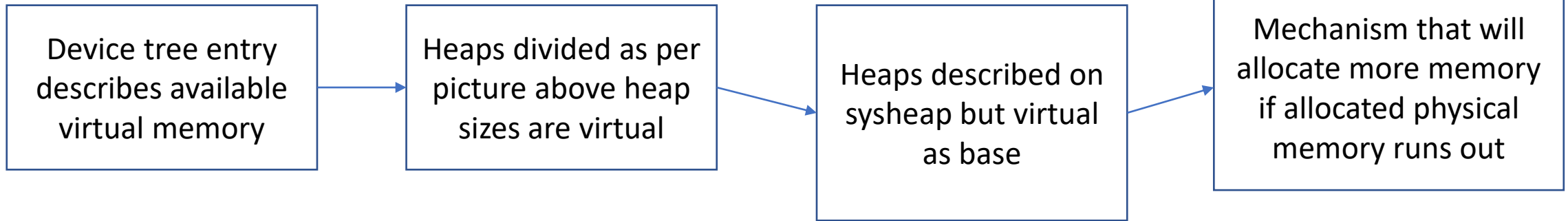
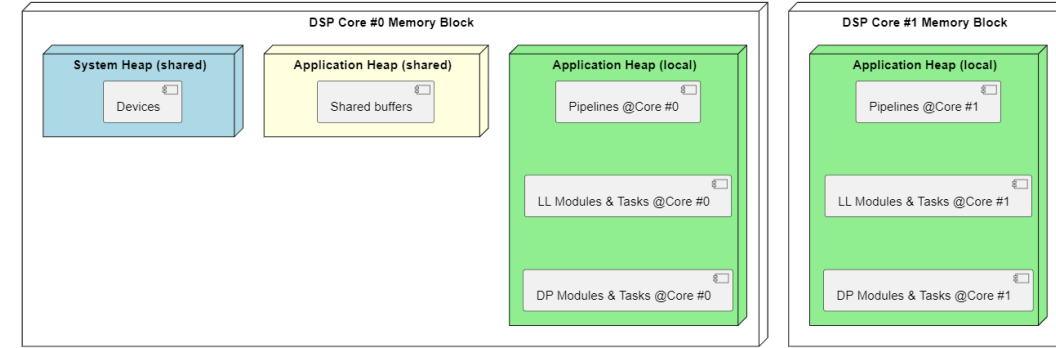
Pipelines @Core #1

LL Modules & Tasks @Core #1

DP Modules & Tasks @Core #1



Proposition



- Separate application heaps allow for cache operations such as invalidates to be minimized since it is a big issue In multi core environment
- Virtual sizes allow for overbooking and greater flexibility for integrators to mess around with topologies on different cores
- Memory mapped by MMD in runtime allows to save the power – we power what we need